1/12 FIG. 1

3 APPLICATION ~1A CONSOLE STORAGE MANAGEMENT 5 ~1B CN3-CN1 2 -SVP 10 - CN2 CHA CN11 CHA -11 13 14 15 CONTROL MEMORY CACHE MEMORY **SWITCH PART** T1 T2 12~ -12 DKA DKA 16H 16H 16H 16H -18 17H-LDEV LDEV **~18** CN12 ~ **CN12** 16L 16L 16L 16L LDEV 17L ·

	GROUP#	LDEV#	USE STATUS		DISK ATTRIBUTE	
		# 01	USED			
,	<b>*</b> #	# 05	USED	Š		701/170
	- #	# 03	USED	ב ב ב ב	AIGA SPEED INTERINAL DEVICE	DEVICE
FIG. 2A		# 04	RESERVABLE			
		# 05	USED			
71	#2	1 90#	RESERVABLE	) MOT	LOW SPEED INTERNAL DEVICE	DEVICE
		40 /	USED			
	#3	80#	RESERVABLE	SMOT	LOW SPEED EXTERNAL DEVICE	DEVICE
		LDEV #	INHIBIT ATTRIBUTE	RIBUTE	DATE AND TIME OF SETTING	OF SETTING
	FIG. 2B	# 05	WRITE INHIBIT	HBIT	2003/09/01 00:00	1 00:00
		T2~ # 03	READ/WRITE INHIBIT	INHIBIT	2003/09/11 03:30	1 03:30
FIG. 2C						,
# \JOT	READ INHIBIT BIT	IT WRITE INHIBIT BIT	BIT   INQUIRY INHIBIT BIT	IBIT BIT	READ CAPACITY 0 REPORT BIT	S-VOL DISABLE BIT
# 01	0	0	0		0	0
70 #	0	-	0		0	0
# 03	-	-	0		0	0
# 04	0	0	0		0	0
•••	•••	•••	•••		•••	•••

FIG. 3A

OPERATION ATTRIBUT MODE	READ	WRITE	INQUIRY	READ CAPACITY	S-VOL DISABLE
READ/WRITE ENABLE	0	0	0	REAL CAPACITY	0
READ ONLY	0	×	0	REAL CAPACITY	0
READ/WRITE DISABLE	×	×	0	REAL CAPACITY	0
READ CAPACITY 0	×	×	0	"O"	0
INQUIRY INHIBIT	×	×	×	×	0
S-VOL DISABLE	0	0	0	REAL CAPACITY	×

FIG. 3B

		ACCESS ATI	ACCESS ATTRIBUTE CONTROL INFORMATION	FORMATION	
ATTRIBUTE MODE	READ INHIBIT BIT	READ INHIBIT BIT WRITE INHIBIT BIT	INQUIRY INHIBIT BIT	READ CAPACITY 0 REPORT BIT	S-VOL DISABLE BIT
READ/WRITE ENABLE	0	0	0	0	0
READ ONLY	0	_	0	0	0
READ/WRITE DISABLE	1	+	0	0	0
READ CAPACITY 0	1	1	0	1	0
INQUIRY INHIBIT	1	1	1	1	0
S-VOL DISABLE	0	0	0	0	1
RELEASE	0	0	0	0	0

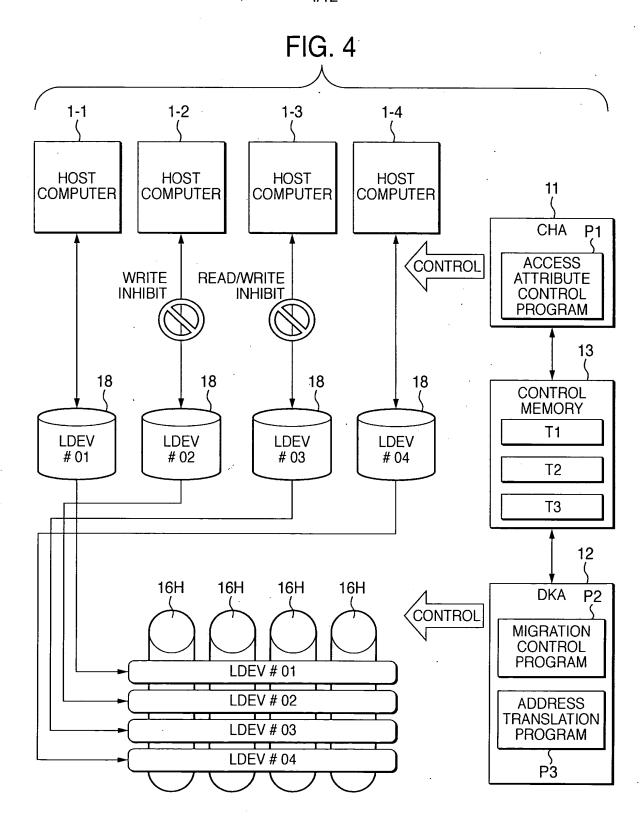
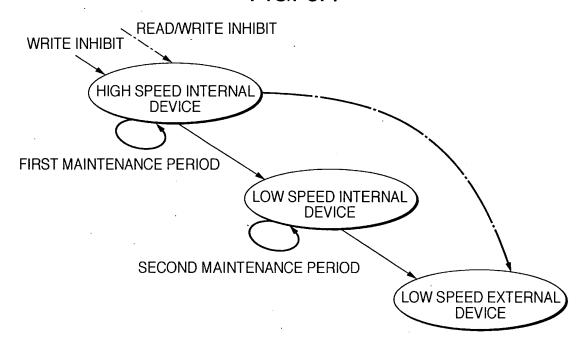


FIG. 5A



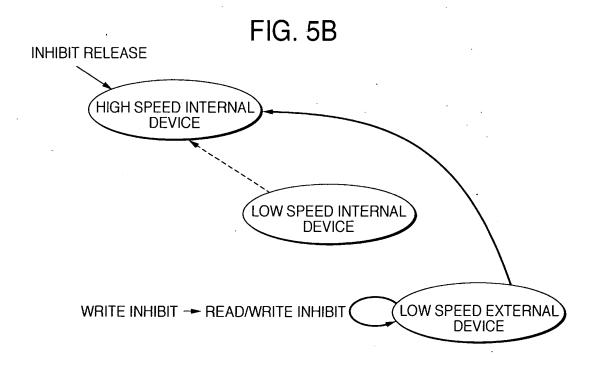


FIG. 6

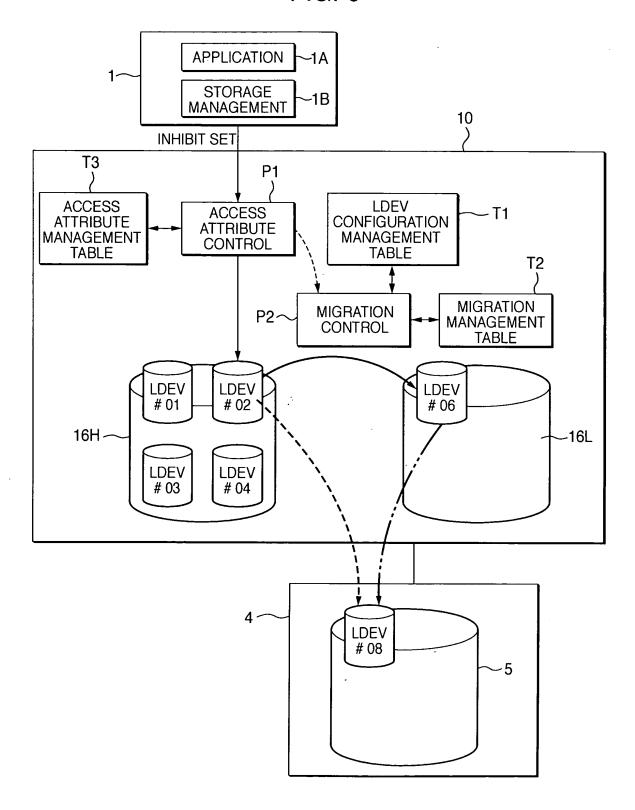


FIG. 7

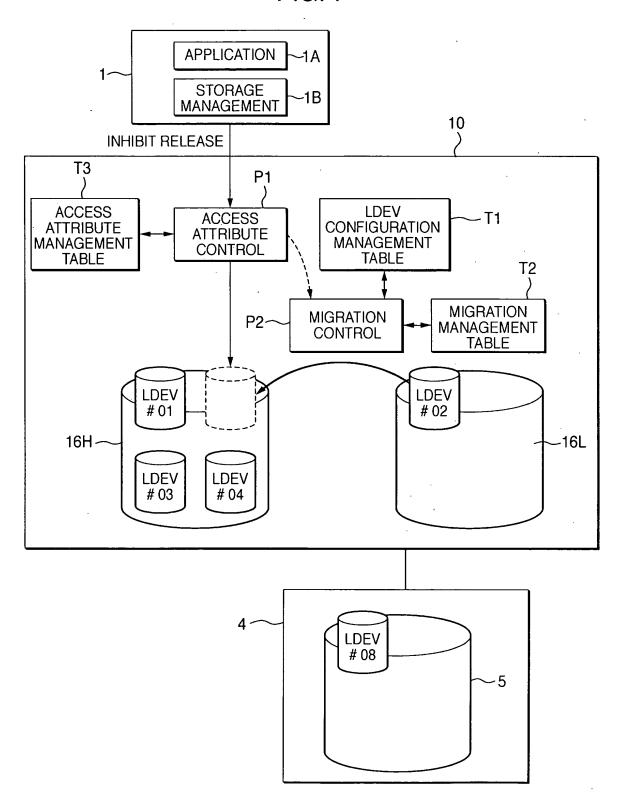
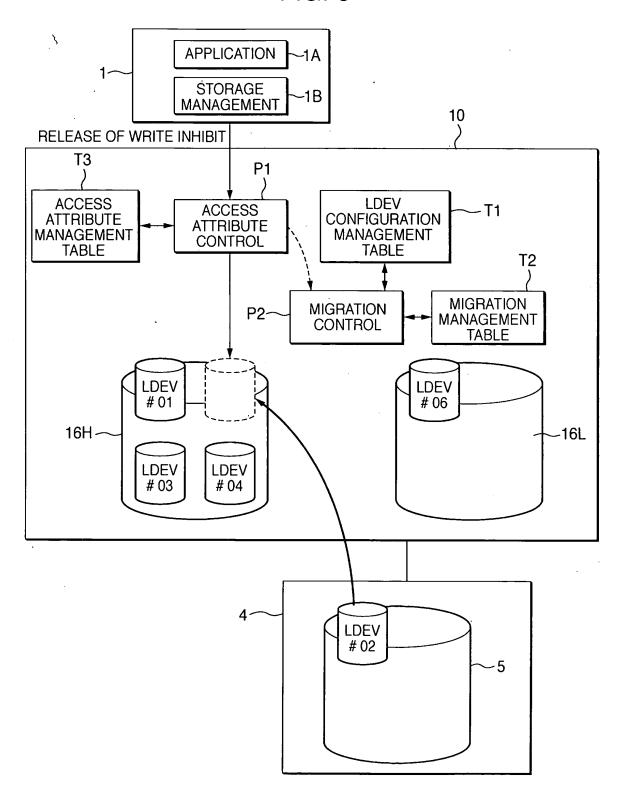
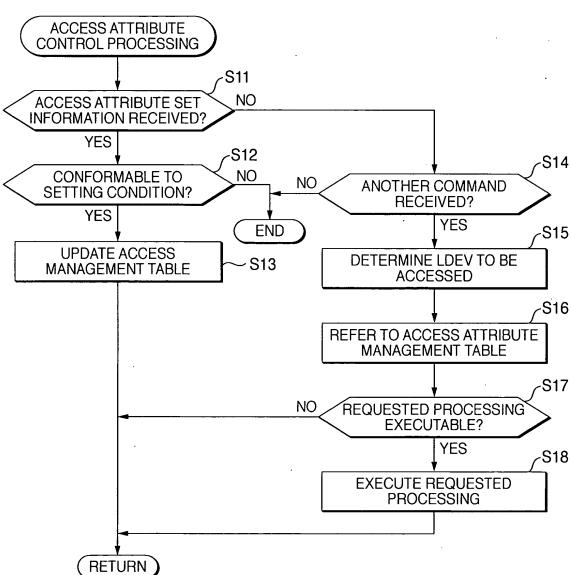


FIG. 8







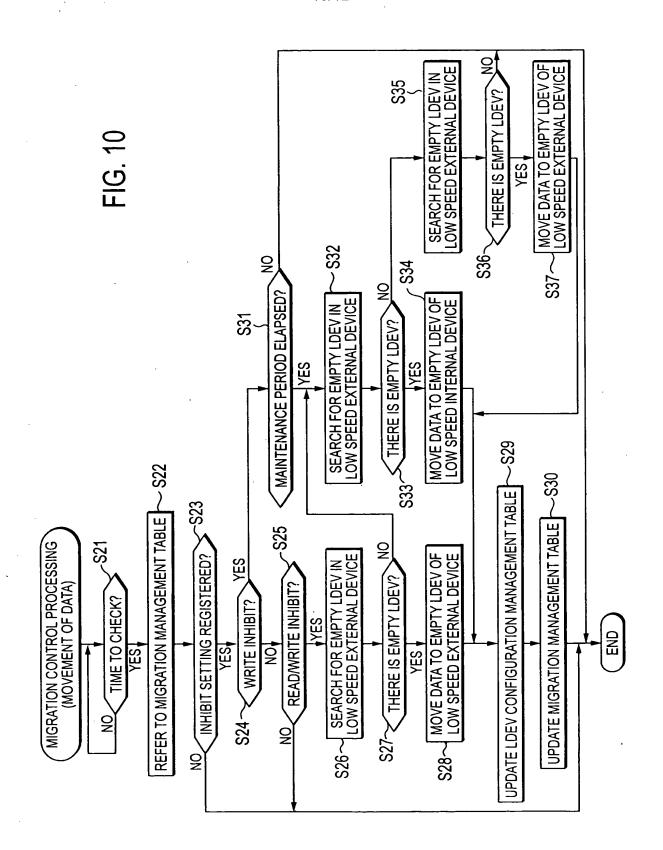
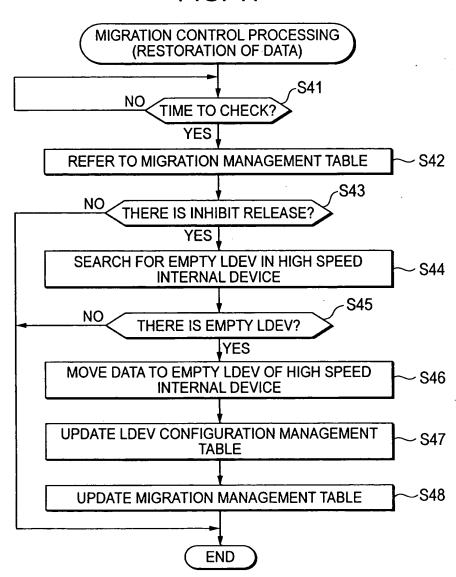


FIG. 11



DISK ATTRIBUTE						LOW SPEED INTERNAL DEVICE	٠,	LOW SPEED EXTERNAL DEVICE
USE STATUS	OSED	RESERVABLE	USED	RESERVABLE	OBSN	RESERVABLE	USED	USED (# 02)
LDEV#	# 01	# 05	# 03	# 04	# 05	. 90#	40 /	# 08
GROUP # 1 # 2 # 3							#3	
				2A		, ,	) - -	
				:1G. 12A				
				$\stackrel{\cdot }{:}$				

	LDEV#	INHIBIT ATTRIBUTE	DATE AND TIME OF SETTING
<u> </u>	# 05		2003/12/01 00:00
	# 03	READ/WRITE INHIBIT	2003/09/11 03:30

C	•	)
	7	J
7		
<u> </u>	_	)
Ĺ	L	

	<u> </u>		~_T3	!		
	S-VOL DISABLE BIT	0	0	.0	0	•••
	READ CAPACITY 0 REPORT BIT	0	0	0	0	•••
	NHIBIT BIT WRITE INHIBIT BIT INQUIRY INHIBIT BIT	0	0	0	0	•••
	WRITE INHIBIT BIT	0	0		0	•••
	READ INHIBIT BIT	0	0	_	0	•••
)	LDEV #	# 01	# 05	# 03	# 04	•••